



## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

<b>Title of Invention</b>	Optimizing exit latency from an active power management state																																												
<p>Application Number: 10/749619 Confirmation Number: 8290 First Named Applicant: David Puffer Attorney Docket Number: 42P17994 Art Unit: 2185 Examiner: Not Known Search string: ( 5890004 or 6021506 or 6131167 or 6282665 ).pn.</p> <p><b>US Patent Documents</b></p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>ref</td><td>1</td><td>5890004</td><td>1999-03-30</td><td>Poisner</td><td></td><td></td><td></td></tr><tr><td>ref</td><td>2</td><td>6021506</td><td>2000-02-01</td><td>Cho</td><td></td><td></td><td></td></tr><tr><td>ref</td><td>3</td><td>6131167</td><td>2000-10-10</td><td>Cruz</td><td></td><td></td><td></td></tr><tr><td>ref</td><td>4</td><td>6282665</td><td>2001-08-28</td><td>Cruz</td><td></td><td></td><td></td></tr></tbody></table> <p><b>Signature</b></p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td>Nitin C. Patel</td><td>4/21/06</td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	ref	1	5890004	1999-03-30	Poisner				ref	2	6021506	2000-02-01	Cho				ref	3	6131167	2000-10-10	Cruz				ref	4	6282665	2001-08-28	Cruz				Examiner Name	Date	Nitin C. Patel	4/21/06
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